



Indira Gandhi Delhi Technical University For Women

Department of Electronics and Communication Engineering

ANALOG ELECTRONICS LABORATORY



FACULTY INCHARGE

Dr. Richa Yadav

TECHNICAL ASSISTANT

Mr. Madhur Gupta



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FACILITIES (HARDWARE)

S. NO.	EQUIPMENT	QUANTITY
1	LAB TABLE (SIZE: 5 X 3 X 3 – MADE OF TEAK WOOD)	09
2	STOOL (WOODEN)	25
3	PSX 1200 AMPLIFIER	01
4	CORDLESS MIC	02
5	BREADBOARD TRAINER KIT (TRINITY)	05
6	PN / ZENER DIODE CHARACTERISTICS KIT (MARS)	02
7	TRANSISTOR CHARACTERISTICS KIT (MARS)	02
8	TRANSISTOR CHARACTERISTICS TRAINER (SILICOM)	02
9	FET CHARACTERISTICS KIT (MARS)	02
10	SINGLE STAGE RC COMMON EMITTER TRANSISTOR AMPLIFIER KIT (MARS)	02
11	TWO STAGE RC COUPLED AMPLIFIER KIT (MARS)	02
12	TWO STAGE RC COUPLED AMPLIFIER & EMITTER FOLLOWER (SILICOM)	02
13	RECTIFIER & FILTER (HW, FW, BRIDGE – WITH / WITHOUT AMPLIFIER) – SILICOM	02
14	LOGIC GATES TRAINER (SILICOM)	06
15	TRAINER TO STUDY CHARACTERISTICS OF SCR, DIAC & TRIAC (SILICOM)	02



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FACILITIES (HARDWARE)

Contd.

S. NO.	EQUIPMENT	QUANTITY
16	TRAINER TO STUDY CHARACTERISTICS OF MOSFET, FET & UJT (SILICOM)	02
17	ANALOG VSWR METER (SILICOM)	06
18	MULTIPLE POWER SUPPLY (SILICOM)	05



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B.TECH - ECE-AI (SEMESTER-I), ECE (SEMESTER-III)

SUBJECT CODE: BEC-101, BEC-201

Room No.- E-315

LIST OF EXPERIMENTS

1. To study the front panel of Digital Storage Oscilloscope (DSO), various Active & passive components, Breadboard, and Digital multimeter.
2. To study of testing of following Active & Passive components using Digital multimeter: (a) Multimeter probe (b) Connecting wire (c) resistance (d) Silicon diode (e) Light Emitting Diode (f) NPN and PNP transistor (g) Identify Emitter, Base and Collector in NPN and PNP transistor.
3. To plot the forward and reverse characteristics of the Silicon diode and determine its Dynamic and Static resistance.
4. To plot the forward and reverse characteristics of the Zener diode and determine its Dynamic and Static resistance.
5. To plot the input/output characteristics of a given transistor in CE (Common Emitter) configuration.
6. To plot the input/output characteristics of a given transistor in CB (Common Base) configuration.
7. To plot the Drain & transfer characteristics of a Field Effect Transistor (FET).
8. To plot the positive & negative biasing characteristics curve of a Diode for Alternating Current (DIAC).



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DO'S AND DON'TS

DO'S

- Enter and leave the lab as per the time table.
- Maintain strict discipline and silence in the lab.
- Proper handling of computer systems must be done.
- Check the connections properly as per circuit diagram before switching on the power supply.
- Be a keen observer while performing experiments in the lab.
- Keep your bags in the rack and the consumable items back to their original position after finishing off the experiment in the lab.

DON'TS

- Do not leave the lab without prior permission of the Lab In-charge or Technical Assistant.
- Do not bring or eat any eatable item in the lab.
- Do not make noise or shout in the lab.
- Do not disturb the decorum or aesthetic view of the lab.
- Do not tamper with the lab or system settings.
- Do not perform the experiment with wet hands on the apparatus.